



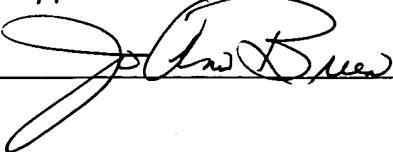
IFW 2614

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date:

7/26/06



PATENT
PWV1.PAU.112

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Richard D. Posner, et al.

Serial No.: 10/052,801

Art Unit: 2614

Filing Date: October 29, 2001

Examiner: Alexander Jamal

Title: Spurious Ratio Control Circuit
for use with Feed-Forward
Linear Amplifiers

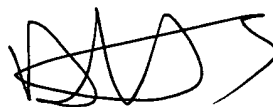
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Enclosed are copies of one (1) Revocation/Appointment of Powers of Attorney and Change of Correspondence Address, and one (1) Statement Under 37 CFR 3.73(b) to be filed in connection with the above-referenced patent application.

Assignee of Record respectfully requests that this change be made of record.

Respectfully submitted,



Date: 7-25-06

David L. Henty
Reg. No. 31,323
Attorney for Assignee of Record

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STATEMENT UNDER 37 CFR 3.73(b)

Applicant/Patent Owner: Richard D. Posner, et al./Powerwave Technologies, Inc.

Application No./Patent No./Control No.: 10/052,801 Filed/Issue Date: October 29, 2001

Entitled: SPURIOUS RATIO CONTROL CIRCUIT FOR USE WITH FEED-FORWARD LINEAR AMPLIFIERS

Powerwave Technologies, Inc., a corporation

(Name of Assignee)

(Type of Assignee: corporation, partnership, university, government agency, etc.)

states that it is:

1. ☒ the assignee of the entire right, title, and interest; or
2. ☐ an assignee of less than the entire right, title and interest
(The extent (by percentage) of its ownership interest is _____ %)

in the patent application/patent identified above by virtue of either:

- A. ☒ An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel 012975, Frame 0387, or a true copy of the original assignment is attached.

OR

- B. ☐ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as follows:

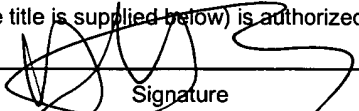
1. From: _____ To: _____
The document was recorded in the United States Patent and Trademark Office at
Reel _____, Frame _____, or for which a copy thereof is attached.
2. From: _____ To: _____
The document was recorded in the United States Patent and Trademark Office at
Reel _____, Frame _____, or for which a copy thereof is attached.
3. From: _____ To: _____
The document was recorded in the United States Patent and Trademark Office at
Reel _____, Frame _____, or for which a copy thereof is attached.

☐ Additional documents in the chain of title are listed on a supplemental sheet.

As required by 37 CFR 3.73(b)(1)(i), the documentary evidence of the chain of title from the original owner to the assignee was, or concurrently is being, submitted for recordation pursuant to 37 CFR 3.11.

[NOTE: A separate copy (i.e., a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, to record the assignment in the records of the USPTO. See MPEP 302.08]

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.


Signature
David L. Henty (Reg. No. 31,323)
Printed or Typed Name

7-25-06
Date
(949) 223-9600
Telephone Number

Attorney of Record for Assignee
Title

This collection of information is required by 37 CFR 3.73(b). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

REVOCATION/APPOINTMENT OF POWERS OF ATTORNEY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Powerwave Technologies, Inc., a corporation, located at 1801 E. St. Andrew Place, Santa Ana, California, 92705, United States of America, and the owner of all rights, title and interest in the issued patents and patent applications, as set forth in Schedule A, by virtue of Assignment, hereby revoke all previous powers of attorneys and hereby appoint in their stead:

David L. Henty, Reg. No. 31,323
Richard L. Myers, Reg. No. 26,490
Daniel L. Dawes, Reg. No. 27,123
Joseph C. Andras, Reg. No. 33,469
Kenneth L. Sherman, Reg. No. 33,783
Michael Zarrabian, Reg. No. 39,886
Vic Y. Lin, Reg. No. 43,754

As its attorney(s) to prosecute said applications and to transact in connection therewith all business in the U.S. Patent and Trademark Office and before competent International Authorities.

Please change the correspondence address and direct all future correspondence to:

Myers Dawes Andras & Sherman, LLP
19900 MacArthur Blvd., Suite 1150
Irvine, CA 92612
Attn: David L. Henty, Esq.
Tel: (949) 223-9600

POWERWAVE TECHNOLOGIES, INC.

Dated: July 5, 2006

By Perry Tarnofsky
Name: Perry Tarnofsky
Title: Vice President of Legal Affairs

SCHEDULE A

<u>Patent No.</u>	<u>Issue Date</u>	<u>Attorney Docket No.</u>	<u>Title</u>
6,856,215	Feb. 15, 2005	PWV1.PAU.109	System and Method for Adjusting Group Delay
6,897,724	May 24, 2005	PWV1.PAU.111	System and Method for Adjusting Group Delay
6,818,477	Nov. 16, 2004	PWV1.PAU.113	Method of Mounting a Component in an Edge-Plated Hole Formed in a Printed Circuit Board
6,937,094	August 30, 2005	PWV1.PAU.114	Systems and Methods of Dynamic Bias Switching for Radio Frequency Power Amplifiers
6,794,954	Sept. 21, 2004	PWV1.PAU.115	Microstrip Coupler
6,952,147	Oct. 14, 2005	PWV1.PAU.116	Microstrip Coupler
6,949,992	Sept. 27, 2005	PWV1.PAU.117	System and Method of Providing Highly Isolated Radio Frequency Interconnections
6,946,906	Sept. 20, 2005	PWV1.PAU.119	System and Method of Pilot Tone Reuse in a Feedforward Amplifier
6,958,647	Oct. 25, 2005	PWV1.PAU.120	Dual Loop Feedforward Power Amplifier

<u>Application No.</u>	<u>Application Date</u>	<u>Attorney Docket No.</u>	<u>Title</u>
60/314,840	Aug. 24, 2001	PWV1.PAU.109.0	Group Delay Adjusting Circuit
60/325,627	Sept. 28, 2001	PWV1.PAU.112.0	Spurious Detection Circuit for Use with Feed Forward Linear Amplifiers
60/333,643	Nov. 26, 2001	PWV1.PAU.113.0	Edge Plated Transistor Well
60/428,607	Nov. 22, 2002	PWV1.PAU.114.0	Systems and Methods of Dynamic Bias Switching for Radio Frequency Power Amplifiers
10/052,801	Oct. 29, 2001	PWV1.PAU.112	Spurious Ratio Control Circuit for Use with Feed-Forward Linear Amplifiers
10/805,634	March 19, 2004	PWV1.PAU.110	System and Method for Adjusting Group Delay
10/442,608	May 21, 2003	PWV1.PAU.118	Spurious Energy Correlation for Control of Linear Power Amplifiers
10/723,802	Nov. 25, 2003	PWV1.PAU.121	System and Method of Carrier Rejection in a Feed Forward Amplifier
10/227,687	Aug. 26, 2002	PWV1.PAU.126	Low Cost Highly Isolated RF Coupler